

- Version History**
- v2 hardwire LED1 to GPIO 40
 - v2 hardwire LED2 to GPIO 41
 - v2 hardwire SW1 to GPIO 24
 - v2 hardwire SW2 to GPIO 39
 - v2 hardwire SW3 to WAKEUP_0
 - v2 hardwire SW4 to WAKEUP_1
 - v2 removed ANT_DIV_CTRL_0 and ANT_DIV_CTRL_1 from the headers
 - v2 remove QSPI_CLK, QSPI_SSN, QSPI_D{0,1,2,3} from the headers
 - v2 remove CON4 and CON5 connections, and the R38 and R29 resistors
 - v2 added UART MUX
 - v2 added JTAG Link Block
 - v2 Modified PIO Headers

